

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|------------------------------------|------------------|---------|------------------|
| L1 | 1 | (thread near circuit) and (programmable near logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:22 |
| L2 | 2 | (thread near circuit) and (logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:23 |
| L3 | 107 | (thread same circuit) and (logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:23 |
| L4 | 75 | (thread same circuit) and (programmable near logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:23 |
| L5 | 29 | (thread same circuit) and (programmable near logic near device) and (control near data) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:24 |
| L6 | 39 | (thread same circuit) and (logic near device) and (control near data) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:26 |
| L7 | 10 | (thread same circuit) with (programmable near logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:31 |
| L8 | 14 | (thread same circuit) and (logic near device) and (control near logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:33 |
| L9 | 1 | (thread near circuit) and (programmable near logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:57 |
| L10 | 1 | (thread near circuit) and (program\$5 near logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:58 |
| L11 | 80 | (thread same circuit) and (program\$5 near logic near device) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:59 |

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| L12 | 201 | (thread same circuit) and (program\$5 near logic) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | OFF | 2008/12/16 11:59 |
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